

FIGURE 1

10/021,565
Allen et al.

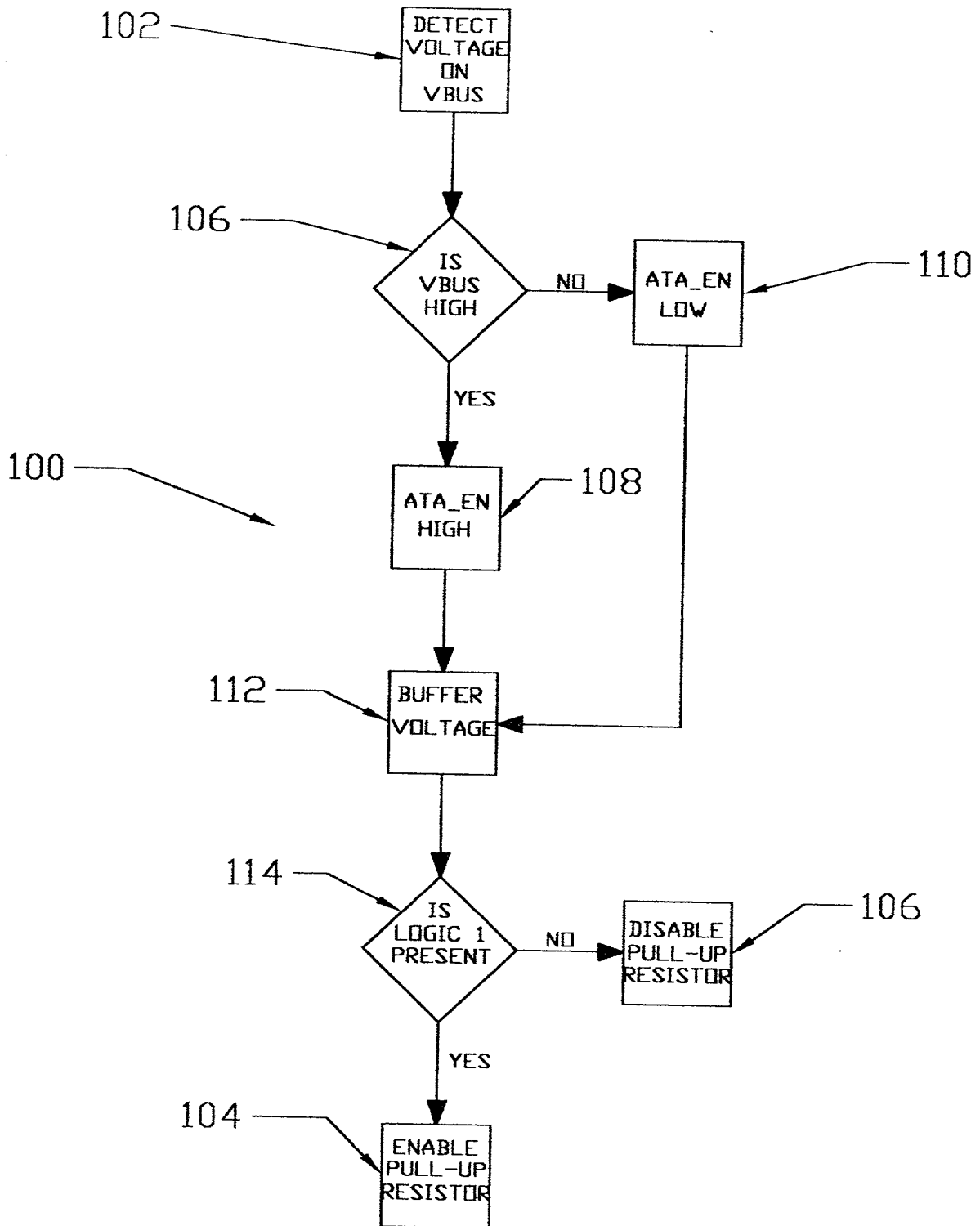


FIGURE 2

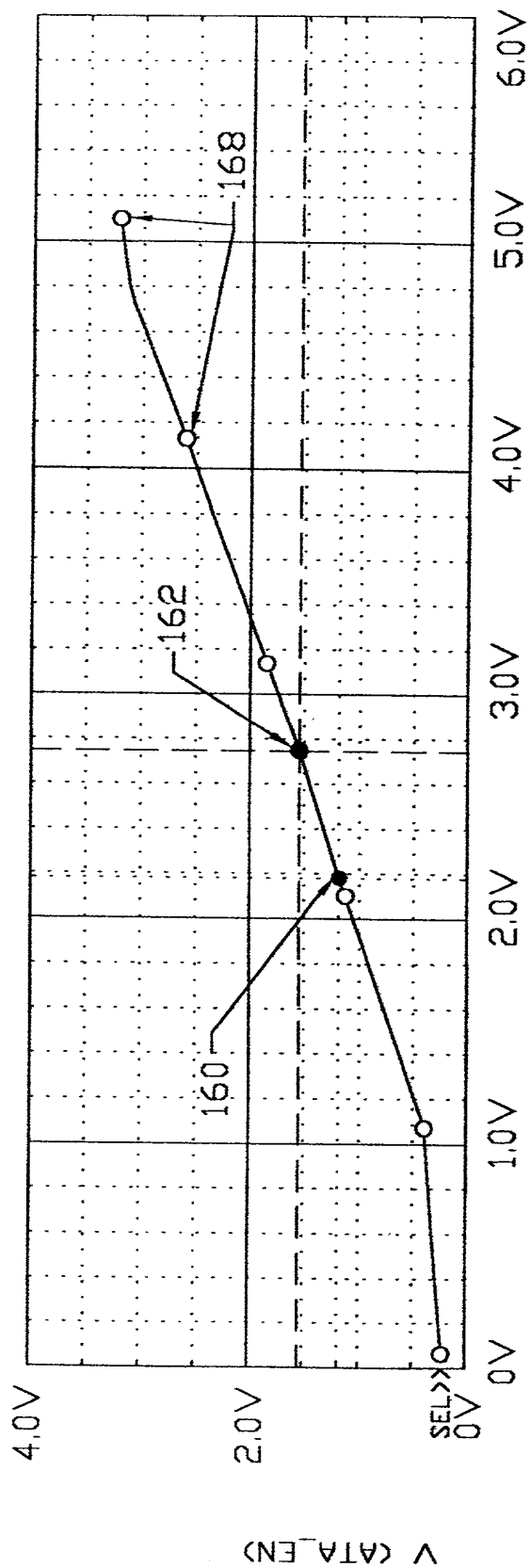
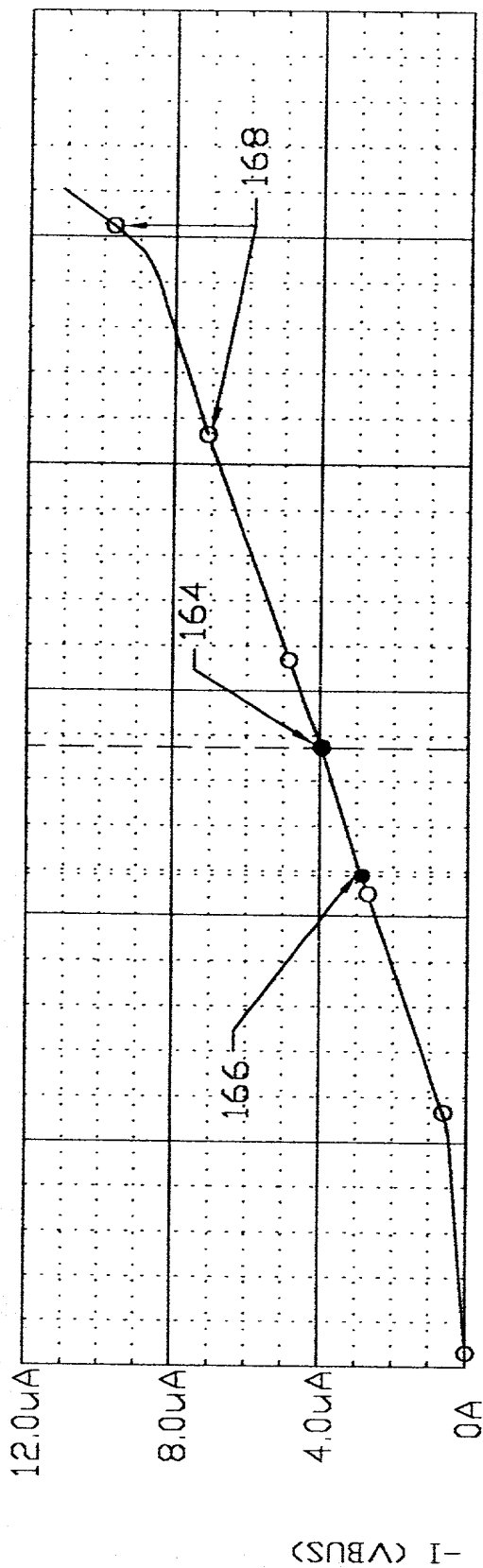


FIGURE 3

V_{VBUS}

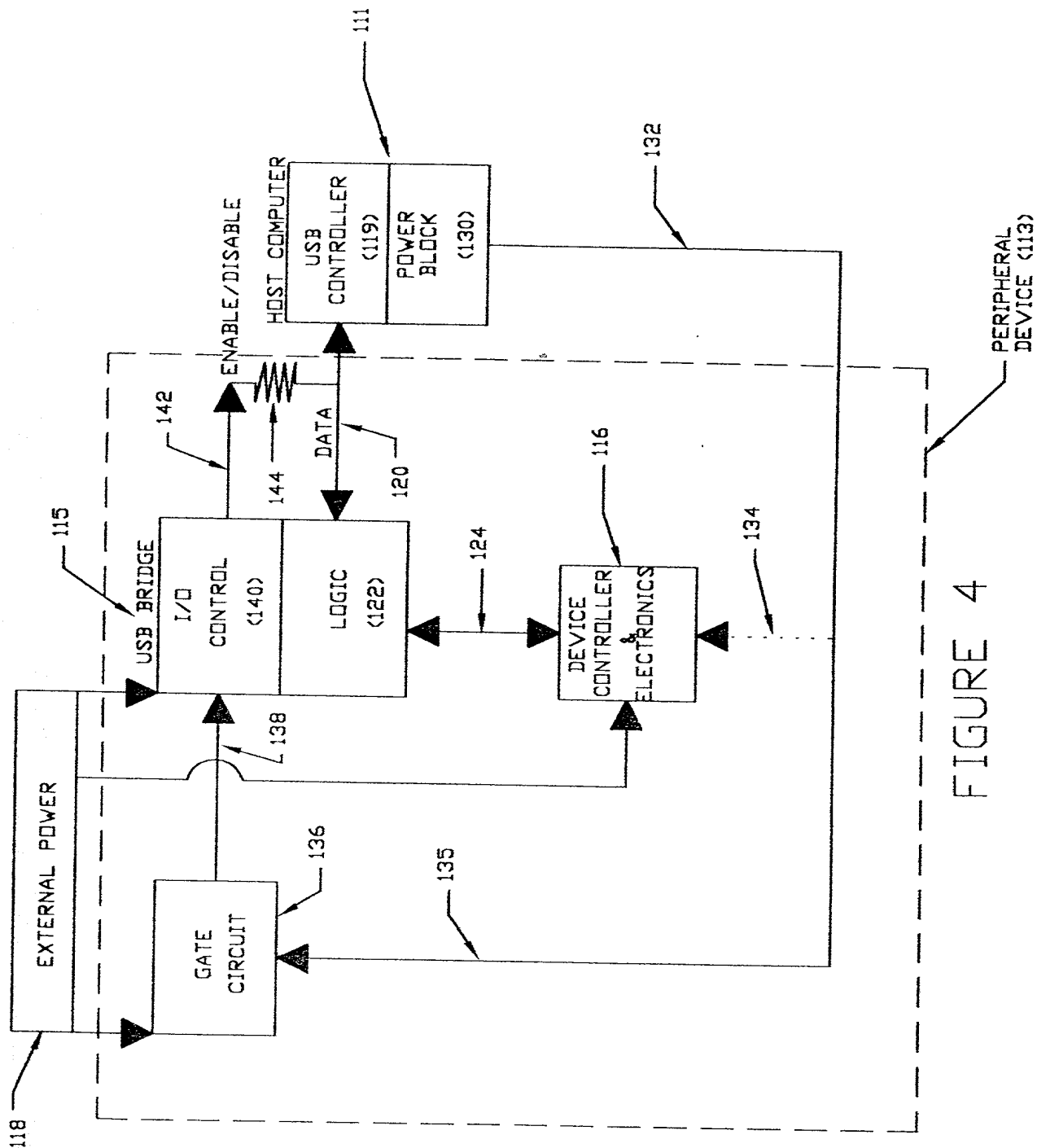


FIGURE 4

EXT POWER	BRIDGE LOCK	I/O CONTROL	PULL-UP RESISTOR	DATA LINE	POWER BUS	STATUS
NO	WORKING	1	ENABLE	TRANSMIT	YES/HIGH	A 150
YES	WORKING	0	DISABLE	NON-ACTIVE	NO/LOW	B 152
----	DORMANT	OFF	N/A	NON-ACTIVE	-----	C 154

TRI-STATE MATRIX

FIGURE 5